



Address:
JAMBU PERSHAD & SONS
6275/22 Nicholson Road,
Ambala Cantt, Haryana,
INDIA
Pin: 133001

Email:
sales@japson.com
japsonambala@yahoo.com

Website:
www.japson.com
Phone:
+91-171-4006897

Boolean Identities/Demorgan's Theorems Apparatus

Product Image



Description

To verify the truth table of 3 input AND gate, NAND gate, NOR gate ,Single input NOT gates, Boolean expressions and Demorgan's Theorems. Instrument comprises of DC power supply, 4 switch for selecting logic '1' and logic '0', 2 red LED output indicators, circuit diagram printed for 5 "OR", 5 "AND" and 4 "NOT" gates and their connections brought out at sockets.

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Catalog No. 101008

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.