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Logic Gates Experiment Kit

Product Image



Description

To Verify the truth table for AND, NAND, OR, NOR, NOT, EX-OR, EX-NOR . Instrument comprises of 5V Regulated power supply for Logic "1" & Logic "0". TTL ICs are used for Gates & Component Soldered on the front Panel & Connections Brought out at Sockets.

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Logic Gates Experiment Kit

Catalog No. 101029

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.