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Study of Logarithmic Amplifier Experiment Apparatus

Product Image



Description

Objective : To study the Log. Antilog and ideality factor for Log Amplifier and plot a graph for Antilog Amplifier.

Features : Instrument comprises of DC Regulated short circuit and over-load protected continuously variable Power Supply, ± 15 VDC Regulated Power Supply, one Potentiometer, IC 741 and Diode 4007/IN 34.

Catalog No. Particulars

JE21150 Study of Logarithmic Amplifier Experiment Apparatus

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.